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#
# This file purports to specify one complete configuration
# of the SVT hardware, for both cold start and simulation,
# referring to Punzi-format files as much as possible.
#
# This will evolve, after Punzi/Belforte/Ashmanskas dialog.
#
# 2001-03-16, WJA
#
# Some notes:
#   - File is parsed into an array of (key,value) pairs of strings
#   - Parsed data can be queried for existence/value, given key
#   - These key/value pairs may be useful for other kinds of files
#   - A CRC of the parse tree itself can be computed (omitting one key)
#   - If an expected CRC is zero, it is treated as a wildcard
#   - Map set does not contain board settings that do not plausibly
#     need to be modeled by the offline trigger simulation
#
fileType        svtMapset
fileVersion     0.1
mapsetName      nom_regular_XFTM_20010316
mapsetCrc       0
#
# Wedge 0
#
w00_pattFile    302_nom_0123X_A_20000830.patt
w00_ssFile                  302_20000830.ss
w00_ifitFile             tf_w00_20010314.ifit
w00_useqImage           56_hold_20001023.useq
w00_crcAMuseq   1974008812
w00_crcAMssmp   3140526603
w00_crcAM0pat   3563447812
w00_crcAM1pat   2750454568
w00_crcHBssmp   1441537785
w00_crcHBpatt   3991062931
w00_crcTFaddr   2932381494
w00_crcTF0cft   0
w00_crcTF0icp   0
w00_crcTF1cft   0
w00_crcTF1icp   0
w00_crcTF2cft   0
w00_crcTF2icp   0
w00_crcTF3cft   0
w00_crcTF3icp   0
w00_crcTF4cft   0
w00_crcTF4icp   0
w00_crcTF5cft   0
w00_crcTF5icp   0
w00_crcTFshft   671123376
w00_crcTFssx0   999029689
#
# Wedge 1
#
w01_pattFile    302_nom_0123X_B_20000830.patt
w01_ssFile                  302_20000830.ss
w01_ifitFile             tf_w01_20010314.ifit
w01_useqImage           56_hold_20001023.useq
w01_crcAMuseq   1974008812
w01_crcAMssmp   3140526603
w01_crcAM0pat   3563447812
w01_crcAM1pat   2750454568
w01_crcHBssmp   1441537785
w01_crcHBpatt   3991062931
w01_crcTFaddr   2932381494
w01_crcTF0cft   0
w01_crcTF0icp   0
w01_crcTF1cft   0
w01_crcTF1icp   0
w01_crcTF2cft   0
w01_crcTF2icp   0
w01_crcTF3cft   0
w01_crcTF3icp   0
w01_crcTF4cft   0
w01_crcTF4icp   0
```