SVT commissioning progress

Bill A, for SVT group, 2000-09-07 DAQ meeting

- Goal: SVT init/read via R_C; match bunch id SIXD/SVTD
- All SVT boards now have coldstart in FER, but some still no-op
- b0svt01 was in integration runs Aug 30, Sep 1 (init but no readout)
- Need L2B from XTRP, but workaround (hardware kludge) exists
- Read Jane's L2 track board into SVTD (1 dummy track/event), in sync with L1 trigger, via R_C, in run 95000 (Aug 31)
- Both SVX DAQ hardware and L2 track board are in needed by multiple subsystem tests, so resource allocation can be tricky
- We think the above goal can be met (better late than never) this weekend, once (a) coldstart code is fleshed out a bit more and (b) L2 track board is freed up from Magic bus I/O tests